# Related imageSIR SYED UNIVERSITY OF ENGINEERING & TECHNOLOGY

**COMPUTER ENGINEERING DEPARTMENT**

# CE215L DIGITAL LOGIC DESIGN

**Project Approval Form Date: \_ / / 2024**

**B**ATCH: **C**OMPUTER ENGG **20**

**I \_ \_** as a group leader my section is hereby do accept all the rules and regulations setup for the course Basic Electronics Semester project by the relevant teachers.

Project Title:

# Group Members:

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Serial No** | **Name of Students** | **Roll Numbers** | **Email Address/Cell Number** | **Sign** |
| 1 |  |  |  |  |
| 2 |  |  |  |  |
| 3 |  |  |  |  |
| 4 |  |  |  |  |

**Teacher Name: Miss Syeda Bushra Shabeeh**

**Signature:** Date

APPROVED

NOT APPROVE